

	L #	Hit	S arch Text	DBs
1	L1	3173	(361/760,782,783,784,785,786,790,803).ccls.	USP AT; US-P GPU B
2	L2	2589	(365/51,52,63,185.09).ccls.	USP AT; US-P GPU B
3	L3	625	(710/301,302).ccls.	USP AT; US-P GPU B
4	L4	974	326/30.ccls.	USP AT; US-P GPU B
5	L5	704	(257/664,728).ccls.	USP AT; US-P GPU B
6	L6	7966	1 2 3 4 5	USP AT; US-P GPU B
7	L8	197	6 and @pd>=20030603	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DBs
8	L10	626	361/777.ccls.	USP AT; US-P GPU B
9	L11	1513	174/261.ccls.	USP AT; US-P GPU B
10	L12	1924	10 11	USP AT; US-P GPU B
11	L13	34	12 and @pd>=20030603	USP AT; US-P GPU B
12	L14	15	13 and (terminat\$4 impedance\$1 match\$3 finger\$1)	USP AT; US-P GPU B
13	L9	105	8 and (terminat\$4 impedance\$1 match\$3 finger\$1)	USP AT; US-P GPU B

	L #	Hits	S arch T xt
1	L1	219	361/788.ccls.
2	L2	125	1 and (terminat\$4 impedance\$1 match\$3 finger\$1)
3	L3	14	1 and stub\$2

7/26/03 EAST 1.04